AMENDMENTS TO THE CLAIMS

1. (Previously presented) Signal delaying device for the dynamic delaying of a digitally sampled input signal with a memory element and a series connected interpolation element, comprising a register that has its output side connected to the input side of the interpolation element for the intermediate storage of at least one sampled value ($S_{in}(k)$) of the input signal and is arranged in parallel to the memory element, and a marking device which, after a sampled value ($S_{in}(k)$) of the input signal has been placed in intermediate storage in the register, adds a marking to the next sampled value ($S_{in}(k+1)$) of the input signal stored in the memory element.

2. (Canceled)

3. (Previously presented) Signal delaying device according to claim 1, characterized in that the interpolation element checks whether the marking has arrived at the output of the memory element, and following this, reads out a sampled value (x(k)) from the memory element and also a sampled value from the register.

4. (Currently amended) Signal delaying device ~~for the dynamic delaying of a digitally sampled input signal with a memory element and a series connected interpolation element,~~ according to claim 1, comprising a register having its output side connected to the input side of the interpolation element for the intermediate storage of at least one sampled value ($S_{in}(k)$) of the input signal and arranged in parallel to the memory element, wherein the interpolation element comprises a polyphase filter.

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5. (Previously presented) Signal delaying device according to claim 4, wherein the interpolation element comprises a half-band filter, which is arranged between the memory element and the register on one side, and the polyphase filter on the other side.

6. (Previously presented) Method for the dynamic delaying of a digitally sampled input signal with the following procedural stages:

- storing sampled values of the input signal in a memory element,
- reading out of the sampled values ($S_{in}(k)$) from the memory element,
- interpolating the sampled values (x(k)) read out from the memory element, wherein
- whenever the range defined by two successive sampled values (x(k-4), x(k-3)) is neither undercut nor exceeded in the interpolation, one sampled value ($S_{in}(k)$) is placed into the memory element and one sampled value (x(k)) is read out from the memory element,
- whenever the range defined by two successive sampled values (x(k-4), x(k-3)) is exceeded in the interpolation, no new sampled value (x(k)) is read out from the memory element,
- before the range defined by two successive sampled values (x(k-4), x(k-3)) is undercut in the interpolation, placing a sampled value ($S_{in}(k)$) of the input signal in intermediate storage in a register arranged in parallel to the memory element, marking the next sampled value ($S_{in}(k+1)$) of the input signal stored in the memory element, and reading out a sampled value from the memory element and also the sampled value placed in intermediate storage in the register, whenever the marked sampled value arrives at the output of the memory element.

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7. (Previously presented) Method according to claim 6, wherein the range defined by two successive sampled values (x(k-4), x(k-3)) is exceeded, if at least two interpolation values (S_{out}(k-3), S_{out}(k-2)) produced by the interpolation fall within this range.

8. (Previously presented) Method according to claim 6 or 7, wherein the range defined by two successive sampled values (x(k-4), x(k-3)) is undercut in the interpolation, if no interpolation value produced by the interpolation falls within this range.

9. (Previously presented) Method according to any one of claims 6-7, wherein storage in the memory element takes place by means of a write pointer, and reading out from the memory element takes place by means of a read pointer, wherein the write pointer and the read pointer in each case point towards a given memory cell of the memory element,

wherein the write pointer and also the read pointer are adjusted if the range defined by two successive sampled values (x(k-4), x(k-3)) is neither undercut nor exceeded in the interpolation.

10. (Previously presented) Method according to claim 9, wherein only the write pointer but not the read pointer is adjusted, if the range defined by two successive sampled values (x(k-4), x(k-3)) is exceeded in the interpolation.

11. (Previously presented) Method according to claim 9, wherein only the read pointer but not the write pointer is adjusted, if a sampled value is stored in the register.

12. (Previously presented) Method according to claim 9, wherein both the write pointer and also the read pointer are adjusted, if a sampled value is read out from the register.

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